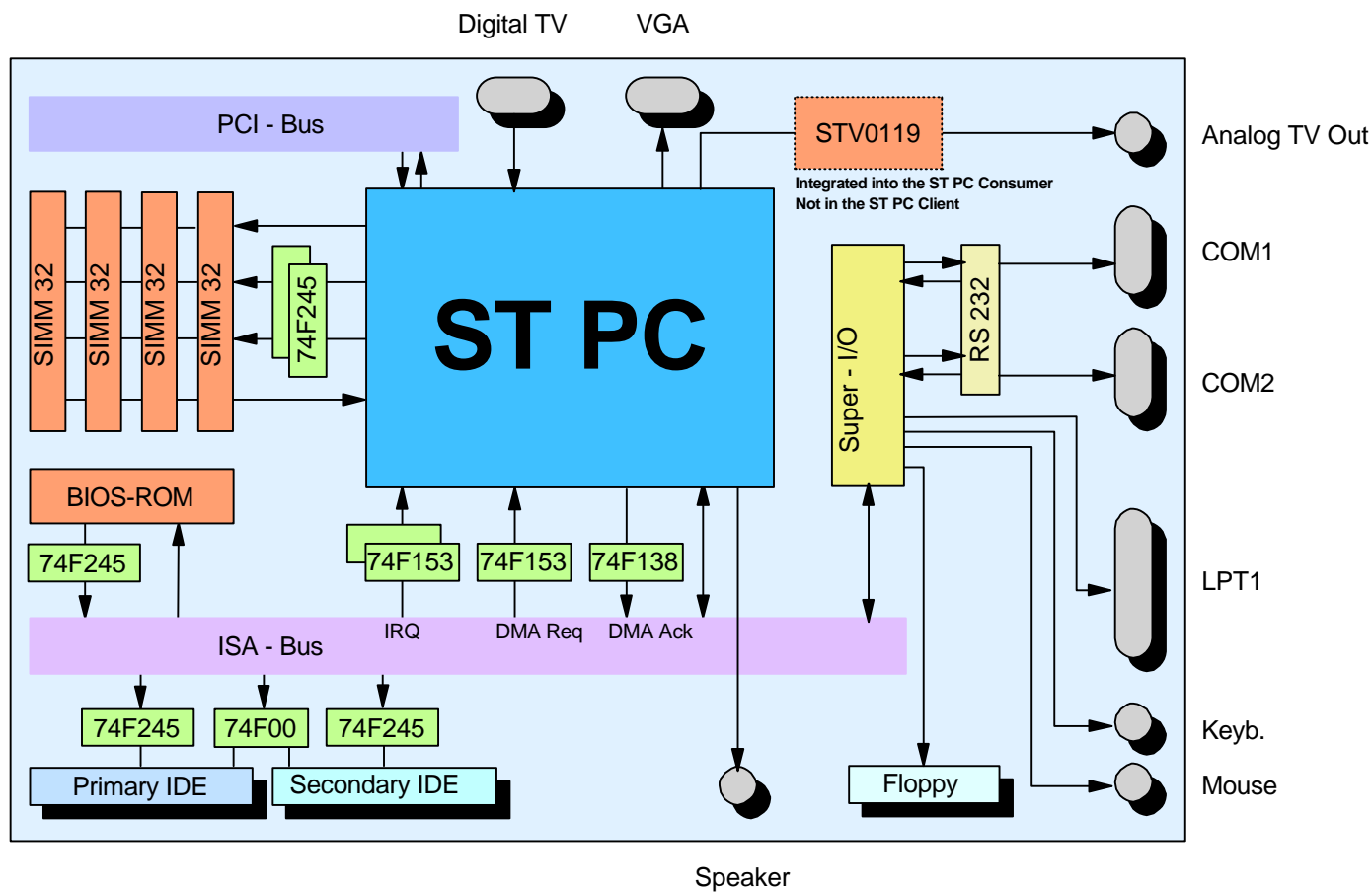
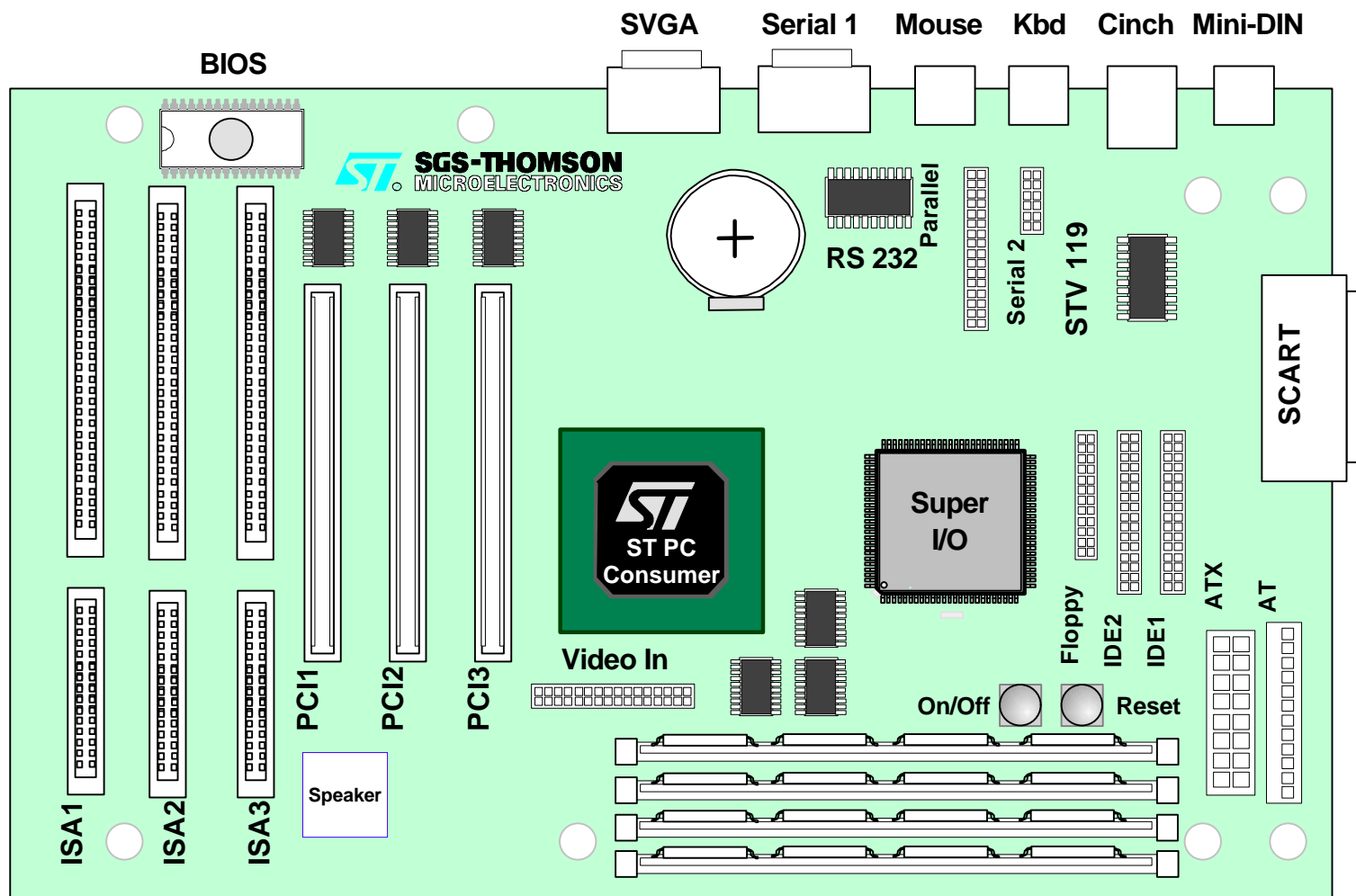


STPC Client / STPC Consumer

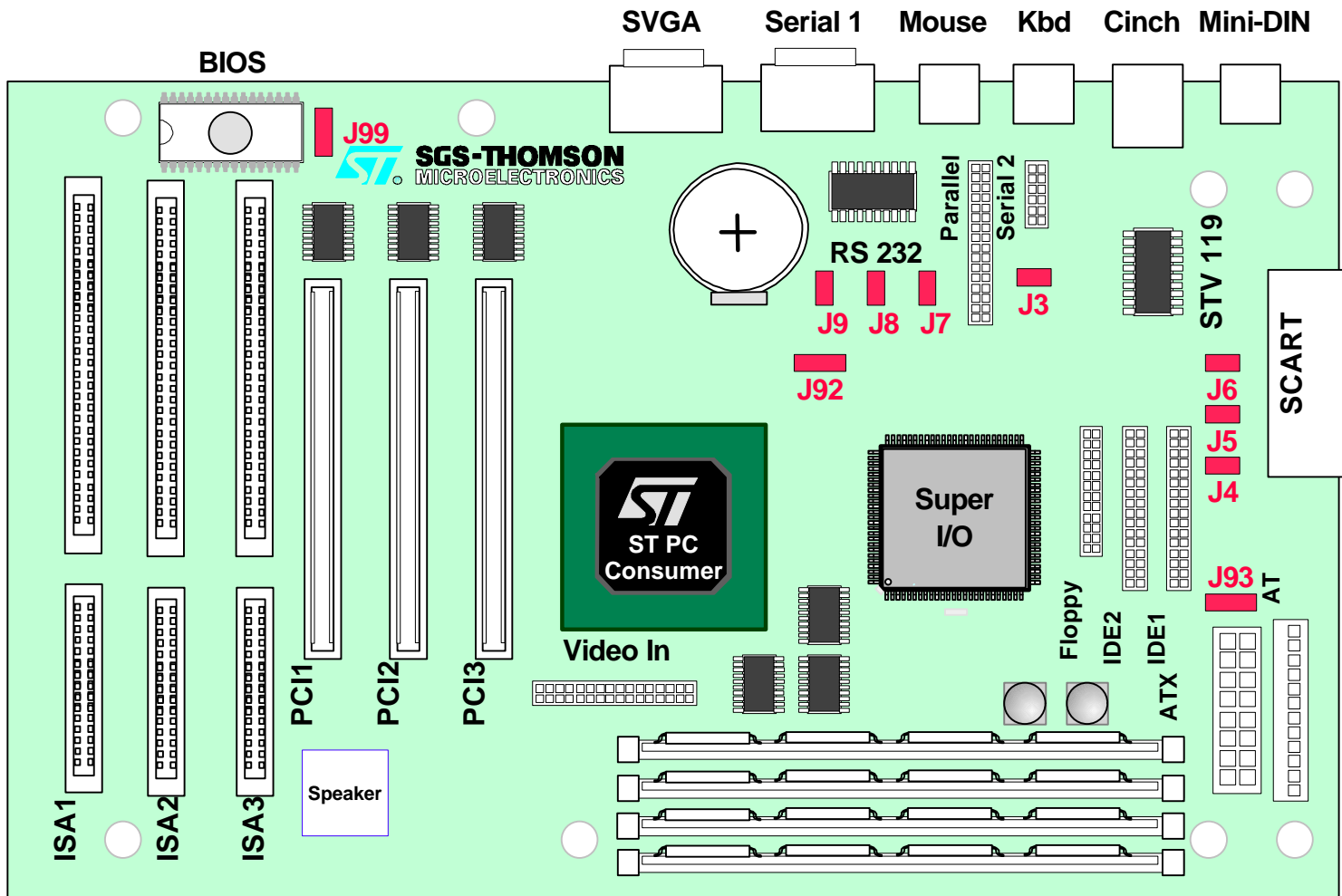
Evaluation board - EVMINI 1.1



STPC Client / STPC Consumer Evaluation Board - EVMINI 1.1



Configuration Jumpers



J[3]: TV Output

- [0] : CVBS

+ [X] : RGB

J[4:6]: TV Output (default is [X00])

+ [X00] : Amplified CVBS/SYNC to SCART.

- [0X0] : CVBS/SYNC to SCART.

- [00X] : STPC.HSYNC to STV0119

J[7:8]: Super I/O Base Address

- [00] : 0x398

- [0X] : 0x26E

```
- [X0] : 0x15C
```

+ [XX] : 0x2E

J[9]: Super I/O activated Functions

```
- [0] : FDC, LPT, KBC, RTC, COM1, IDE
```

+ [X] : FDC, LPT, KBC, RTC, COM1, COM2

J[92] : Backup Voltage

+ [1-2] : Battery On.

- [2-3] : Clear Setup

J[93] : ATX Power control

- [1-2] : Force ATX On

- + [2-3] : Switch & Soft control

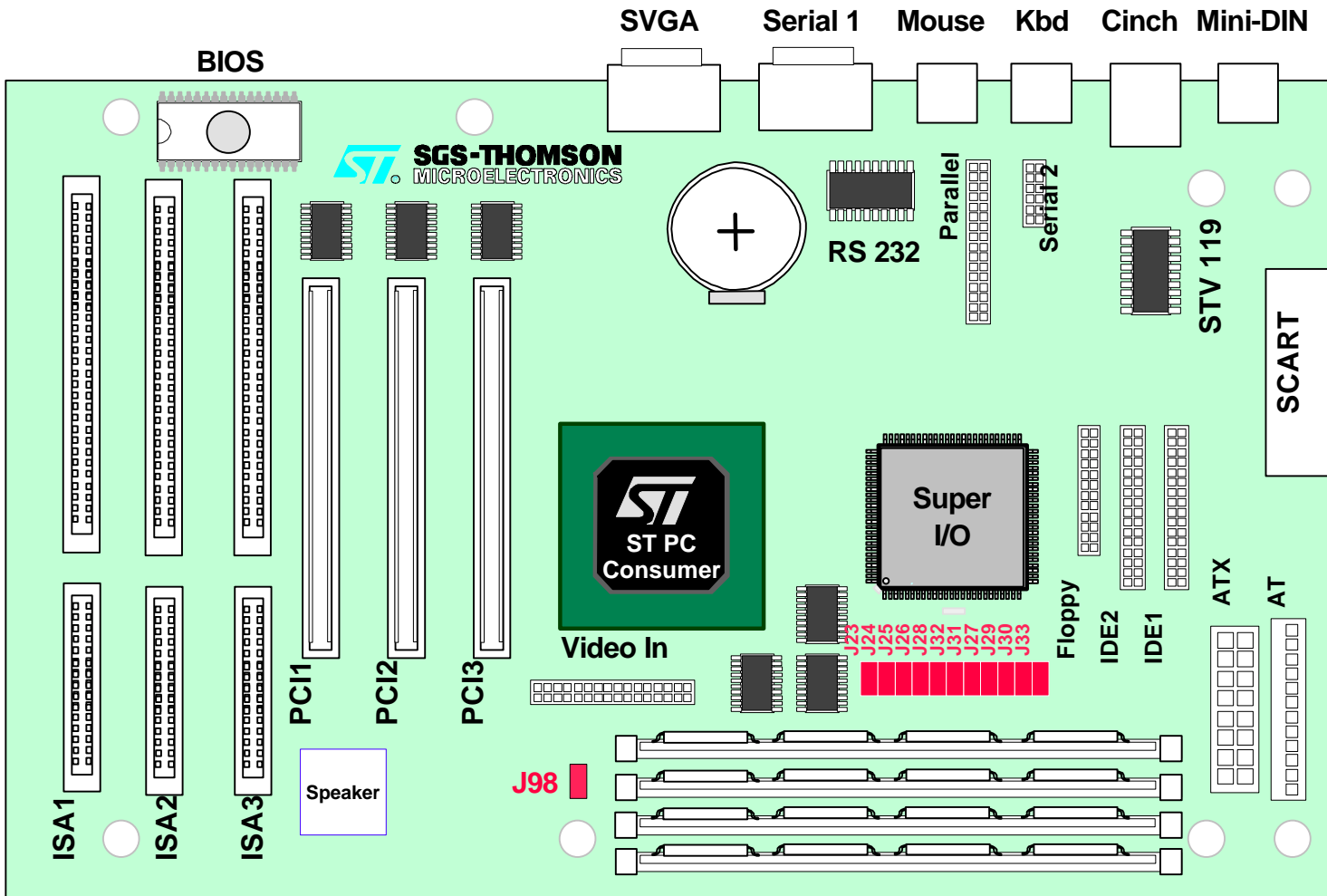
- [0] : Force ATX Off

J[99]: PROM Size

- [1-2] : 1Mb

+ [2-3] : 2Mb

Strap Options



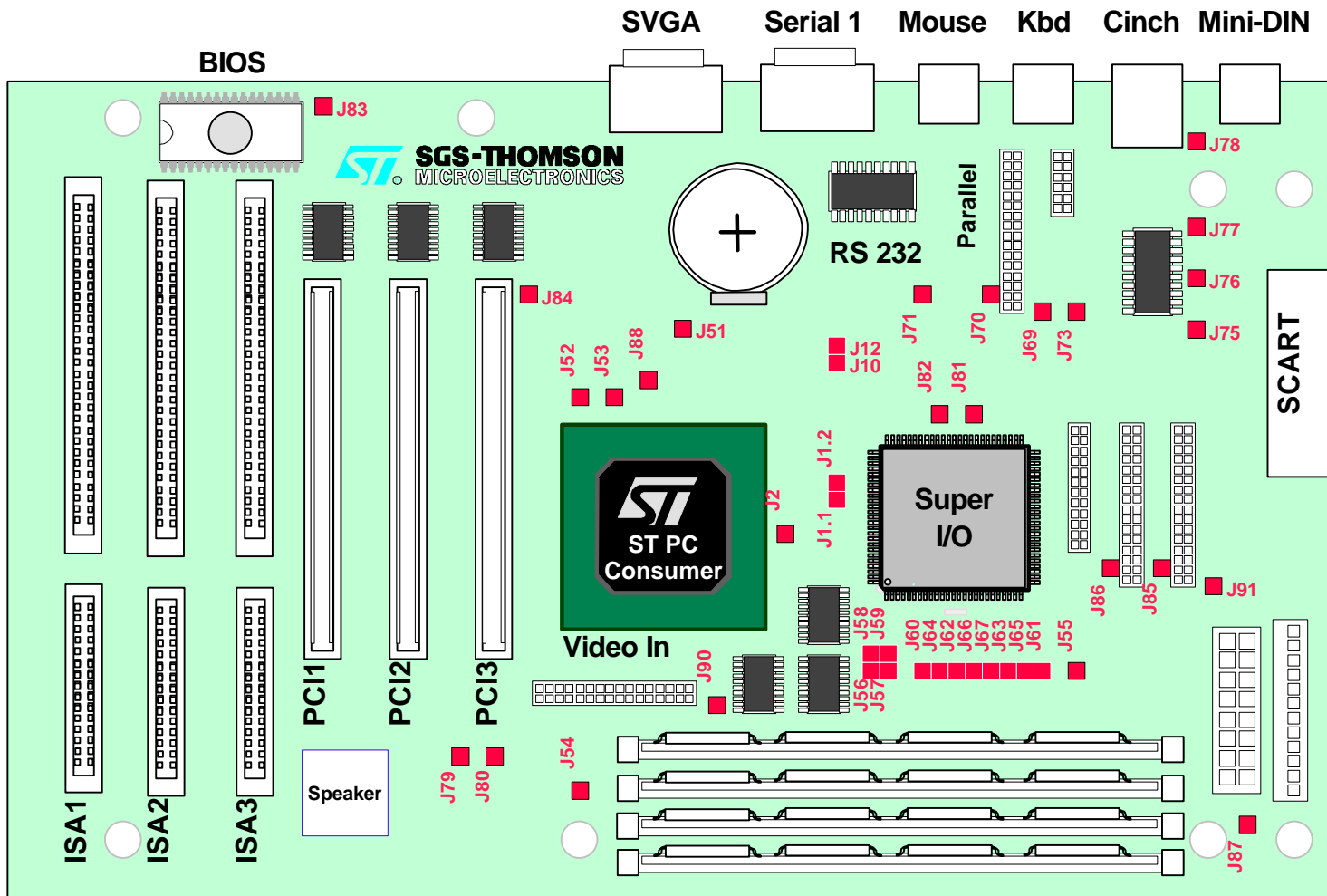
```
J[26]: Bank 0 is FPM [0] or EDO [X].
J[32]: Bank 1 is FPM [0] or EDO [X].
J[27]: Bank 2 is FPM [0] or EDO [X].
J[30]: Bank 3 is FPM [0] or EDO [X].
```

```
J[28]: Bank 0 is 60ns [0] or 70ns [X].
J[31]: Bank 1 is 60ns [0] or 70ns [X].
J[29]: Bank 2 is 60ns [0] or 70ns [X].
J[33]: Bank 3 is 60ns [0] or 70ns [X].
```

- [XXX] : Reserved.
- [XX0] : Reserved.
- [X0X] : Reserved.
- [X00] : 25 MHz.
- [0XX] : 50 MHz.
- [0X0] : 60 MHz.
- + [00X] : 66 MHz.
- [000] : 75 MHz.

```
- [0] : HClk / 3
+ [1] : HClk / 2
```

Test points



J1.1, J51, J54, J87 : GND
 J69 : AGND
 J74 : VDDA
 J68 : VCCA

J1.2 : HCLK
 J52, J53 : Quartz (SIP110 only)

J[59:56] : RAS#[0:3]
 J[60:67] : CAS#[0:7]
 J55 : WE#

J76, J75, J77, J78 : R, G, B, CVBS
 J71 : STV0119 Clock
 J72 : VRef CVBS
 J73 : VRef RGB

J79 : ISACKL2X
 J80 : ISACKL (SYSCLK)

J81 : FlashEnable#
 J82 : GPIOWR#

J83 : RMRCCS#

J84 : ISAOE#
 J85 : PIRQ
 J86 : SIRQ

J88 : RTCAS# (SIP110 only)
 J89 : SysRst (Active high)
 J90 : MainOsc (Oscillator Output)
 J91 : PowerGood